



SREE SAKTHI ENGINEERING COLLEGE

TNEA Admission Code **2673**

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COIMBATORE- 641104. INDIA | Web : www.sreesakthi.edu.in

Affiliated to Anna University & Approved by AICTE, Accredited by NAAC

2.3.1

**STUDENT CENTRIC METHODS
EXPERIENTIAL LEARNING,
PARTICIPATIVE LEARNING**



SREE SAKTHI ENGINEERING COLLEGE

TNEA Admission Code **EG73**

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DEPARTMENT OF CIVIL ENGINEERING

2.3.1	LAB EXPERIMENTAL LEARNING REPORT
	MINI PROJECT REPORT
	MODEL PROJECTS REPORT
	MAIN PROJECT REPORT

A handwritten signature in blue ink, appearing to read 'Dr. R. Prabhu', is positioned above the printed name.

PRINCIPAL
Dr R. PRABHU
PRINCIPAL,
SAKTHI ENGINEERING COL
COIMBATORE-641 104

LAB EXPERIMENTAL LEARNING REPORT:



MATERIALS STERNTH TEST FOR STRENGTH OF MATERIALS



SURVEYING CLASS



EE SAKTHI ENGINEERING COLLEGE, KARAAMADAI, COIMBATORE

DEPARTMENT OF CIVIL ENGINEERING

ACADEMIC YEAR 2021-2022

IV YEAR CIVIL

MINI PROJECT DETAILS

BATCH :2018-2022

S.NO	BATCH	REGISTER NUMBER	NAME	PROJECT TITLE	GUIDE NAME
1	BATCH 1	713618103001	K AJITHKUMAR	PLANNING ANALYSING DESAIGNING AND ESTIMATION OF PRIMARY HEALTH CENTER BUILDING AT PULIAYAKUMALAM	Ms D KIRUBHA
2		713618103006	PARIMALESHWARAN G		
3		713618103008	SARAN M		
4		713618103302	MARGARET SONA M		
5	BATCH 2	713618103002	ARAVIND M A	PLANNING ANALYSIS DESAIGN AND ESTIMATION OF RK MARRAIAGE HALL BUILDING	Mr G ARAVING
6		713618103003	ARIVAZHAGAN S		
7		713618103010	VAISHNAVI S		
8		713618103303	VENKATESH R		
7	BATCH 3	713618103004	KARTHICK PRABHU S	PLANNING ANALYSIS DESAIGN AND ESTIMATION OF RESIDENTIAL BUILDING	Mrs D SHANTHINI
8		713618103005	NANDHINI S		
9		713618103009	SATHISHKUMAR S		
10		713618103301	ANJU R		


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PLANNING, ANALYSIS, DESIGN AND ESTIMATION OF RESIDENTIAL BUILDING

A PROJECT REPORT

Submitted by

KARTHICKPRABHU.S	713618103004
NANDHINIS	713618103005
SATHISH KUMAR.S	713618103009
ANJU.R	713618103301

in partial fulfillment for award of the degree

of

BACHELOR OF ENGINEERING

in

CIVIL ENGINEERING

SREE SAKTHI ENGINEERING COLLEGE, COIMBATORE



ANNA UNIVERSITY::CHENNAI 600025

JANUARY 2022

ANNA UNIVERSITY: CHENNAI 600025

BONAFIDE CERTIFICATE

Certified that this project "PLANNING, ANALYSIS, DESIGN AND ESTIMATION OF RESIDENTIAL BUILDING" is the bonafide work of "KARTHICKPRABHU.S (713618103004), NANDHINI.S (713618103005), SATHISH KUMAR.S (713618103009) and ANJUL.R (713618103301)" who carried out the project work under my supervision.


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Assistant Professor

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Mrs.D.Shanthini

SUPERVISOR

Assistant Professor

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Submitted for the university Examination held on 12/03/2022


Internal Examiner


External Examiner

PLANNING, ANALYSIS, DESIGN AND ESTIMATION OF R K MARRIAGE HALL BUILDING

A PROJECT REPORT

Submitted by

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ARIVAZHAGAN. S	713618103003
VAISHNAVI. K	713618103010
VENKATESH.R	713618103303

in partial fulfillment for award of the degree

of

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CIVIL ENGINEERING

SREE SAKTHI ENGINEERING COLLEGE, COIMBATORE




ANNA UNIVERSITY:CHENNAI 600025

JANUARY 2022

ANNA UNIVERSITY: CHENNAI 600025

BONAFIDE CERTIFICATE

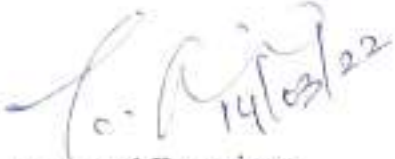
Certified that this project "PLANNING, ANALYSIS, DESIGN AND ESTIMATION OF R K MARRIAGE HALL BUILDING" is the bonafide work of "ARAVIND. MA (713618103002), ARIVAZHAGAN. S (713618103003), VAISHNAVLK (713618103010) and VENKATESH.R (713618103303)" who carried out the project work under my supervision.


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SUPERVISOR
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Department of Civil Engineering
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Karamadai-641104

Submitted for the university Examination held on ...14.03.2022.....


Internal Examiner


External Examiner

**PLANNING, ANALYSING, DESIGNING AND ESTIMATION OF
PRIMARY HEALTH CENTER BUILDING AT PULIYAKULAM**

A PROJECT REPORT

Submitted by

AJITH KUMAR K	713618103001
PARIMALESHWARAN G	713618103006
SARAN M	713618103008
MARGARET SONA M	713618103302

In a partial fulfillment for the award of the degree

Of

BACHELOR OF ENGINEERING

In

CIVIL ENGINEERING

SREE SAKTHI ENGINEERING COLLEGE , KARAMADAI.



ANNA UNIVERSITY, CHENNAI – 600025

JANUARY - 2022

BONAFIDE CERTIFICATE

Certified that this project report on "PLANNING, ANALYSING, DESIGNING AND ESTIMATION OF PRIMARY HEALTH CENTER BUILDING AT PULIYAKULAM" is the bonafide work of the following students who carried out the project work under my supervision.

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PARIMALESHWARAN G	713618103006
SARAN M	713618103008
MARGARET SONA M	713618103302



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M.DEEPIKA B.E.,M.E.

Head of the Department

Department of Civil Engineering
Sree Sakthi Engineering College
Coimbatore -641104



SIGNATURE

D.KIRUBHA B.E.,M.PLAN.

Project Guide

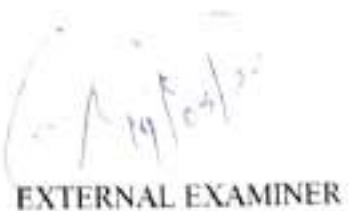
Department of Civil Engineering
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Submitted for the University Examination held on

14/03/2022



INTERNAL EXAMINER



EXTERNAL EXAMINER

MODEL PROJECTS REPORT




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SREE SAKTHI ENGINEERING COLLEGE, KARAAMADAI, COIMBATORE
DEPARTMENT OF CIVIL ENGINEERING
ACADEMIC YEAR 2021-2022

IV YEAR CIVIL

PROJECT DETAILS

BATCH :2018-2022

S.NO	BATCH	REGISTER NUMBER	NAME	DOMAIN	PROJECT TITLE	GUIDE NAME
1	BATCH 1	713618103001	K AJITHKUMAR	MATERIALS STUDY	EXPERIMENTAL INVESTIGATION ON COMPARATIVE STUDY OF REPLACEMENT OF FINE AGGREGATE WITH GRAINED PAPER AND REPLACEMENT OF CEMENT WITH PAPER ASH IN CONCRETE	Ms M DEEPIKA
2		713618103006	PARIMALESHWARAN G			
3		713618103008	SARAN M			
4		713618103302	MARGARET SONA M			
5	BATCH 2	713618103002	ARAVIND M A	MATERIALS STRENGTH	EXPERIMENTAL INVESTIGATION ON STRENGTH PROPERTIES OF CONCRETE BY PARTIAL REPLACEMENT OF CEMENT USING COCONUT ENDOCARP ASH	Mr D SANTHOSH KUMAR
6		713618103003	ARIVAZHAGAN S			
7		713618103301	ANJU R			
8		713618103303	VENKATESH R			
7	BATCH 3	713618103004	KARTHICK PRABHU S	MATERIALS STRENGTH	EXPERIMENTAL INVESTIGATION ON PARTIAL REPLACEMENT OF AGGREGATES IN GEO POLYMER CONCRETE	Mr D SANTHOSH KUMAR
8		713618103005	NANDHINI S			
9		713618103009	SATHISHKUMAR S			
10		713618103010	VAISHNAVI S			


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**EXPERIMENTAL INVESTIGATION ON STRENGTH
PROPERTIES OF CONCRETE BY PARTIAL REPLACEMENT
OF CEMENT USING COCONUT ENDOCARP ASH**

A PROJECT REPORT

Submitted by

ARVIND. M A	713618103002
ARIVAZHAGAN. S	713618103003
ANJU. R	713618103301
VENKATESH. R	713618103303

in partial fulfillment for award of the degree

of

BACHELOR OF ENGINEERING

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CIVIL ENGINEERING

SREE SAKTHI ENGINEERING COLLEGE, COIMBATORE



ANNA UNIVERSITY: CHENNAI 600025

JUNE 2022

ANNA UNIVERSITY: CHENNAI 600025

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Certified that this project “**EXPERIMENTAL INVESTIGATION ON STRENGTH PROPERTIES OF CONCRETE BY PARTIAL REPLACEMENT OF CEMENT USING COCONUT ENDOCARP ASH**” is the bonafide work of “**ARAVIND. MA (713618103002), ARIVAZHAGAN. S (713618103003), ANJU. R (7136181033001) and VENKATESH.R (713618103303)**” who carried out the project work under my supervision.



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Ms.M.DEEPIKA

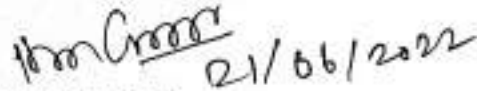
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SUPERVISOR

Assistant Professor

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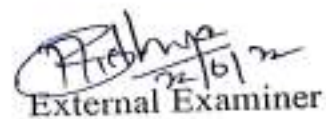
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Karamadai-641104

Submitted for the university Examination held on 22-6-2022



Internal Examiner



External Examiner

**EXPERIMENTAL INVESTIGATION ON
COMPARATIVE STUDY OF REPLACEMENT OF
FINE AGGREGATE WITH GRAINED PAPER AND
REPLACEMENT OF CEMENT WITH PAPER ASH IN
CONCRETE**

A PROJECT REPORT

Submitted by

AJITH KUMAR.K	713618103001
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SARAN.M	713618103008
MARGARET SONA.M	713618103302

*In partial fulfillment for the award of the
degree of*

BACHELOR OF ENGINEERING

in

CIVIL ENGINEERING

SREE SAKTHI ENGINEERING COLLEGE, COIMBATORE.



ANNA UNIVERSITY: CHENNAI 600 025

JUNE 2022

ANNA UNIVERSITY: CHENNAI 600 025

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Certified that this project report "EXPERIMENTAL INVESTIGATION ON COMPARATIVE STUDY OF REPLACEMENT OF FINE AGGREGATE WITH GRAINED PAPER AND REPLACEMENT OF CEMENT WITH PAPER ASH IN CONCRETE" is the Bonafide work of the following students.

AJITH KUMAR.K	713618103001
PARIMALESHWARAN.G	713618103006
SARAN.M	713618103008
MARGARET SONA.M	713618103302

who carried out the project work under my supervision.



Ms.M.DEEPIKA.M.E

Head of the Department

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Ms.M.DEEPIKA,M.E

Project Guide

Department of Civil Engineering

Sree Sakthi Engineering College

Coimbatore

Submitted for the University Examination held at Sree Sakthi Engineering College, Coimbatore on 22/6/22



INTERNAL EXAMINER



EXTERNAL EXAMINER

**EXPERIMENTAL INVESTIGATION ON
PARTIAL REPLACEMENT OF AGGREGATES IN
GEOPOLYMER CONCRETE**

A PROJECT REPORT

Submitted by

KARTHICKPRABHU S	713618103004
NANDHINI S	713618103005
SATHISHKUMAR S	713618103009
VAISHNAVI S	713618103010

in partial fulfillment for award of the degree

of

BACHELOR OF ENGINEERING

in

CIVIL ENGINEERING

SREE SAKTHI ENGINEERING COLLEGE, COIMBATORE



ANNA UNIVERSITY::CHENNAI 600 025

JUNE 2022

ANNA UNIVERSITY: CHENNAI 600 025

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Certified that this project report "EXPERIMENTAL INVESTIGATION ON PARTIAL REPLACEMENT OF AGGREGATES IN GEOPOLYMER CONCRETE" is the bonafide work of KARTHICKPRABHU.S (713618103004), NANDHINI.S(713618103005), SATHISHKUMAR.S(713618103009) and VAISHNAVLK(713618103010)" who carried out the project work under my supervision.



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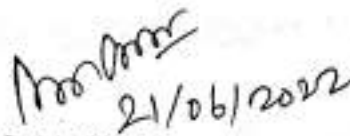
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Assistant Professor

Department of Civil Engineering

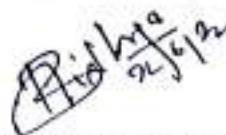
Sree Sakthi Engineering College

Karamadai- 641104

Submitted for the university Examination held on 23.06.2022



Internal examiner



External examiner

EC8361

ANALOG AND DIGITAL CIRCUITS LABORATORY

L	T	P	C
0	0	4	2

OBJECTIVES:

The student should be made to:

- Study the Frequency response of CE, CB and CC Amplifier
- Learn the frequency response of CS Amplifiers
- Study the Transfer characteristics of differential amplifier
- Perform experiment to obtain the bandwidth of single stage and multistage amplifiers
- Perform SPICE simulation of Electronic Circuits
- Design and implement the Combinational and sequential logic circuits

LIST OF ANALOG EXPERIMENTS:

1. Design of Regulated Power supplies
2. Frequency Response of CE, CB, CC and CS amplifiers
3. Darlington Amplifier
4. Differential Amplifiers - Transfer characteristics, CMRR Measurement
5. Cascode and Cascade amplifiers
6. Determination of bandwidth of single stage and multistage amplifiers
7. Analysis of BJT with Fixed bias and Voltage divider bias using Spice
8. Analysis of FET, MOSFET with fixed bias, self-bias and voltage divider bias using simulation software like Spice

9. Analysis of Cascode and Cascade amplifiers using Spice
10. Analysis of Frequency Response of BJT and FET using Spice

LIST OF DIGITAL EXPERIMENTS

1. Design and implementation of code converters using logic gates(i) BCD to excess-3 code and vice versa (ii) Binary to gray and vice-versa
2. Design and implementation of 4 bit binary Adder/ Subtractor and BCD adder using IC 7483
3. Design and implementation of Multiplexer and De-multiplexer using logic gates
4. Design and implementation of encoder and decoder using logic gates
5. Construction and verification of 4 bit ripple counter and Mod-10 / Mod-12 Ripple counters
6. Design and implementation of 3-bit synchronous up/down counter

TOTAL : 60 PERIODS

OUTCOMES:

On completion of this laboratory course, the student should be able to:

- Design and Test rectifiers, filters and regulated power supplies.
- Design and Test BJT/JFET amplifiers.
- Differentiate cascode and cascade amplifiers.
- Analyze the limitation in bandwidth of single stage and multi stage amplifier
- Measure CMRR in differential amplifier
- Simulate and analyze amplifier circuits using PSpice.
- Design and Test the digital logic circuits.

EC8461

CIRCUITS DESIGN AND SIMULATION LABORATORY

L	T	P	C
0	0	4	2

OBJECTIVES:

- To gain hands on experience in designing electronic circuits
- To learn simulation software used in circuit design
- To learn the fundamental principles of amplifier circuits
- To differentiate feedback amplifiers and oscillators.
- To differentiate the operation of various multivibrators

DESIGN AND ANALYSIS OF THE FOLLOWING CIRCUITS

1. Series and Shunt feedback amplifiers-Frequency response, Input and output impedance
2. RC Phase shift oscillator and Wien Bridge Oscillator
3. Hartley Oscillator and Colpitts Oscillator
4. Single Tuned Amplifier
5. RC Integrator and Differentiator circuits
6. Astable and Monostable multivibrators
7. Clippers and Clampers

SIMULATION USING SPICE (Using Transistor):

1. Tuned Collector Oscillator
2. Twin -T Oscillator / Wein Bridge Oscillator
3. Double and Stagger tuned Amplifiers
4. Bistable Multivibrator
5. Schmitt Trigger circuit with Predictable hysteresis
6. Analysis of power amplifier

TOTAL: 60 PERIODS

OUTCOMES:

On completion of this laboratory course, the student should be able to:

- Analyze various types of feedback amplifiers
- Design oscillators, tuned amplifiers, wave-shaping circuits and multivibrators
- Design and simulate feedback amplifiers, oscillators, tuned amplifiers, wave-shaping circuits and multivibrators using SPICE Tool.

OBJECTIVES:

- To understand the basics of linear integrated circuits and available ICs
- To understand the characteristics of the operational amplifier.
- To apply operational amplifiers in linear and nonlinear applications.
- To acquire the basic knowledge of special function IC.
- To use SPICE software for circuit design

DESIGN AND TESTING OF THE FOLLOWING CIRCUITS

1. Inverting, Non inverting and differential amplifiers.
2. Integrator and Differentiator.
3. Instrumentation amplifier
4. Active low-pass, High-pass and band-pass filters.
5. Astable & Monostable multivibrators using Op-amp
6. Schmitt Trigger using op-amp.
7. Phase shift and Wien bridge oscillators using Op-amp.
8. Astable and Monostable multivibrators using NE555 Timer.
9. PLL characteristics and its use as Frequency Multiplier, Clock synchronization
10. R-2R Ladder Type D- A Converter using Op-amp.
11. DC power supply using LM317 and LM723.
12. Study of SMPS

SIMULATION USING SPICE:

1. Active low-pass, High-pass and band-pass filters using Op-amp
2. Astable and Monostable multivibrators using NE555 Timer.
3. A/ D converter
4. Analog multiplier

TOTAL: 60 PERIODS**OUTCOMES:****On completion of this laboratory course, the student should be able to:**

- Design amplifiers, oscillators, D-A converters using operational amplifiers.
- Design filters using op-amp and performs an experiment on frequency response.
- Analyze the working of PLL and describe its application as a frequency multiplier.
- Design DC power supply using ICs.
- Analyze the performance of filters, multivibrators, A/D converter and analog multiplier using SPICE.

EC8562

DIGITAL SIGNAL PROCESSING LABORATORY

L T P C
0 0 4 2

OBJECTIVES:

The student should be made:

- To perform basic signal processing operations such as Linear Convolution, Circular Convolution, Auto Correlation, Cross Correlation and Frequency analysis in MATLAB
- To implement FIR and IIR filters in MATLAB and DSP Processor
- To study the architecture of DSP processor
- To design a DSP system to demonstrate the Multi-rate and Adaptive signal processing concepts.

LIST OF EXPERIMENTS: MATLAB / EQUIVALENT SOFTWARE PACKAGE

1. Generation of elementary Discrete-Time sequences
2. Linear and Circular convolutions
3. Auto correlation and Cross Correlation
4. Frequency Analysis using DFT
5. Design of FIR filters (LPF/HPF/BPF/BSF) and demonstrates the filtering operation
6. Design of Butterworth and Chebyshev IIR filters (LPF/HPF/BPF/BSF) and demonstrate the filtering operations

DSP PROCESSOR BASED IMPLEMENTATION

1. Study of architecture of Digital Signal Processor
2. Perform MAC operation using various addressing modes
3. Generation of various signals and random noise
4. Design and demonstration of FIR Filter for Low pass, High pass, Band pass and Band stop filtering
5. Design and demonstration of Butter worth and Chebyshev IIR Filters for Low pass, High pass, Band pass and Band stop filtering
6. Implement an Up-sampling and Down-sampling operation in DSP Processor

TOTAL: 60 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Carryout basic signal processing operations
- Demonstrate their abilities towards MATLAB based implementation of various DSP systems
- Analyze the architecture of a DSP Processor
- Design and Implement the FIR and IIR Filters in DSP Processor for performing filtering operation over real-time signals
- Design a DSP system for various applications of DSP

EC8561

COMMUNICATION SYSTEMS LABORATORY

L T P C
0 0 4 2

OBJECTIVES:

The student should be made:

- To visualize the effects of sampling and TDM
- To Implement AM & FM modulation and demodulation
- To implement PCM & DM
- To simulate Digital Modulation schemes
- To simulate Error control coding schemes

LIST OF EXPERIMENTS:

1. Signal Sampling and reconstruction
2. Time Division Multiplexing
3. AM Modulator and Demodulator
4. FM Modulator and Demodulator
5. Pulse Code Modulation and Demodulation
6. Delta Modulation and Demodulation
7. Line coding schemes
8. Simulation of ASK, FSK, and BPSK generation schemes
9. Simulation of DPSK, QPSK and QAM generation schemes
10. Simulation of signal constellations of BPSK, QPSK and QAM
11. Simulation of ASK, FSK and BPSK detection schemes
12. Simulation of Linear Block and Cyclic error control coding schemes
13. Simulation of Convolutional coding scheme
14. Communication link simulation

TOTAL: 60 PERIODS**OUTCOMES:****At the end of the course, the student should be able to:**

- Simulate & validate the various functional modules of a communication system
- Demonstrate their knowledge in base band signaling schemes through implementation of digital modulation schemes
- Apply various channel coding schemes & demonstrate their capabilities towards the improvement of the noise performance of communication system
- Simulate end-to-end communication Link

LAB Requirements for a Batch of 30 students (3 students per experiment):

- i) Kits for Signal Sampling, TDM, AM, FM, PCM, DM and Line Coding Schemes
- ii) CROs/DSOs – 15 Nos, Function Generators – 15 Nos.
- iii) MATLAB or equivalent software package for simulation experiments
- iv) PCs - 15 Nos

EC8563**COMMUNICATION NETWORKS LABORATORY**

L	T	P	C
0	0	4	2

OBJECTIVES:**The student should be made to:**

- Learn to communicate between two desktop computers
- Learn to implement the different protocols
- Be familiar with IP Configuration
- Be familiar with the various routing algorithms
- Be familiar with simulation tools

LIST OF EXPERIMENTS:

1. Implementation of Error Detection / Error Correction Techniques
2. Implementation of Stop and Wait Protocol and sliding window
3. Implementation and study of Goback-N and selective repeat protocols
4. Implementation of High Level Data Link Control
5. Implementation of IP Commands such as ping, Traceroute, nslookup.
6. Implementation of IP address configuration.
7. To create scenario and study the performance of network with CSMA / CA protocol and compare with CSMA/CD protocols.
8. Network Topology - Star, Bus, Ring

9. Implementation of distance vector routing algorithm
10. Implementation of Link state routing algorithm
11. Study of Network simulator (NS) and simulation of Congestion Control Algorithms using NS
12. Implementation of Encryption and Decryption Algorithms using any programming language

TOTAL: 60 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Communicate between two desktop computers
- Implement the different protocols
- Program using sockets.
- Implement and compare the various routing algorithms
- Use the simulation tool.

LIST OF EQUIPMENT FOR A BATCH OF 30 STUDENTS

SOFTWARE

- C / Python / Java / Equivalent Compiler
- MATLAB SOFTWARE (Few experiments can be practiced with MATLAB)
- Standard LAN Trainer Kits 4 Nos.
- Network simulator like NS2/ NS3 / Gnomosim/OPNET/ 30 Equivalent

HARDWARE

Standalone Desktops 30 Nos

EC8681 MICROPROCESSORS AND MICROCONTROLLERS LABORATORY L T P C
0 0 4 2

OBJECTIVES:

- To introduce ALP concepts, features and Coding methods
- Write ALP for arithmetic and logical operations in 8086 and 8051
- Differentiate Serial and Parallel interface
- Interface different I/Os with Microprocessors
- Be familiar with MASM

LIST OF EXPERIMENTS:

8086 Programs using kits and MASM

1. Basic arithmetic and Logical operations
2. Move a data block without overlap
3. Code conversion, decimal arithmetic and Matrix operations.
4. Floating point operations, string manipulations, sorting and searching
5. Password checking, Print RAM size and system date
6. Counters and Time Delay

Peripherals and Interfacing Experiments

7. Traffic light controller
8. Stepper motor control
9. Digital clock
10. Key board and Display
11. Printer status
12. Serial interface and Parallel interface
13. A/D and D/A interface and Waveform Generation

8051 Experiments using kits and MASM

14. Basic arithmetic and Logical operations
15. Square and Cube program, Find 2's complement of a number
16. Unpacked BCD to ASCII

TOTAL: 60 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Write ALP Programmes for fixed and Floating Point and Arithmetic operations
- Interface different I/Os with processor
- Generate waveforms using Microprocessors
- Execute Programs in 8051
- Explain the difference between simulator and Emulator

OBJECTIVES:

The student should be made:

- To learn Hardware Descriptive Language (Verilog/VHDL)
- To learn the fundamental principles of VLSI circuit design in digital and analog domain
- To familiarize fusing of logical modules on FPGAs
- To provide hands on design experience with professional design (EDA) platforms

LIST OF EXPERIMENTS:**Part I: Digital System Design using HDL & FPGA (24 Periods)**

1. Design an Adder (Min 8 Bit) using HDL. Simulate it using Xilinx/Altera Software and implement by Xilinx/Altera FPGA
2. Design a Multiplier (4 Bit Min) using HDL. Simulate it using Xilinx/Altera Software and implement by Xilinx/Altera FPGA
3. Design an ALU using HDL. Simulate it using Xilinx/Altera Software and implement by Xilinx/Altera FPGA
4. Design a Universal Shift Register using HDL. Simulate it using Xilinx/Altera Software and implement by Xilinx/Altera FPGA
5. Design Finite State Machine (Moore/Mealy) using HDL. Simulate it using Xilinx/Altera Software and implement by Xilinx/Altera FPGA
6. Design Memories using HDL. Simulate it using Xilinx/Altera Software and implement by Xilinx/Altera FPGA

Compare pre synthesis and post synthesis simulation for experiments 1 to 6.

Requirements: Xilinx ISE/Altera Quartus/ equivalent EDA Tools along with Xilinx/Altera/equivalent FPGA Boards

Part-II Digital Circuit Design (24 Periods)

7. Design and simulate a CMOS inverter using digital flow
8. Design and simulate a CMOS Basic Gates & Flip-Flops
9. Design and simulate a 4-bit synchronous counter using a Flip-Flops
Manual/Automatic Layout Generation and Post Layout Extraction for experiments 7 to 9
Analyze the power, area and timing for experiments 7 to 9 by performing Pre Layout and Post Layout Simulations.

Part-III Analog Circuit Design (12 Periods)

10. Design and Simulate a CMOS Inverting Amplifier.
11. Design and Simulate basic Common Source, Common Gate and Common Drain Amplifiers.
Analyze the input impedance, output impedance, gain and bandwidth for experiments 10 and 11 by performing Schematic Simulations.
Design and simulate simple 5 transistor differential amplifier. Analyze Gain,
12. Bandwidth and CMRR by performing Schematic Simulations.

Requirements: Cadence/Synopsis/ Mentor Graphics/Tanner/equivalent EDA Tools

TOTAL :60 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Write HDL code for basic as well as advanced digital integrated circuit
- Import the logic modules into FPGA Boards
- Synthesize Place and Route the digital IPs
- Design, Simulate and Extract the layouts of Digital & Analog IC Blocks using EDA tools

EC8711

EMBEDDED LABORATORY

L T P C
0 0 4 2

OBJECTIVES:

The student should be made to:

- Learn the working of ARM processor
- Understand the Building Blocks of Embedded Systems
- Learn the concept of memory map and memory interface
- Write programs to interface memory, I/Os with processor
- Study the interrupt performance

LIST OF EXPERIMENTS:

1. Study of ARM evaluation system
2. Interfacing ADC and DAC.
3. Interfacing LED and PWM.
4. Interfacing real time clock and serial port.
5. Interfacing keyboard and LCD.
6. Interfacing EPROM and interrupt.
7. Mailbox.
8. Interrupt performance characteristics of ARM and FPGA.
9. Flashing of LEDs.
10. Interfacing stepper motor and temperature sensor.
11. Implementing zigbee protocol with ARM.

TOTAL: 60 PERIODS

OUTCOMES:

At the end of the course, the student should be able to:

- Write programs in ARM for a specific Application
- Interface memory, A/D and D/A converters with ARM system
- Analyze the performance of interrupt
- Write program for interfacing keyboard, display, motor and sensor.
- Formulate a mini project using embedded system

EC8761

ADVANCED COMMUNICATION LABORATORY

L T P C
0 0 4 2

OBJECTIVES:

The student should be made to:

- Understand the working principle of optical sources, detector, fibers
- Develop understanding of simple optical communication link
- Understand the measurement of BER, Pulse broadening
- Understand and capture an experimental approach to digital wireless communication
- Understand actual communication waveforms that will be sent and received across wireless channel

LIST OF OPTICAL EXPERIMENTS

1. Measurement of connector, bending and fiber attenuation losses.
2. Numerical Aperture and Mode Characteristics of Fibers.
3. DC Characteristics of LED and PIN Photo diode.
4. Fiber optic Analog and Digital Link Characterization - frequency response(analog), eye diagram and BER (digital)

LIST OF WIRELESS COMMUNICATION EXPERIMENTS

1. Wireless Channel Simulation including fading and Doppler effects
2. Simulation of Channel Estimation, Synchronization & Equalization techniques
3. Analysing Impact of Pulse Shaping and Matched Filtering using Software Defined Radios
4. OFDM Signal Transmission and Reception using Software Defined Radios

LIST OF MICROWAVE EXPERIMENTS

1. VSWR and Impedance Measurement and Impedance Matching
2. Characterization of Directional Couplers, Isolators, Circulators
3. Gunn Diode Characteristics
4. Microwave IC – Filter Characteristics

TOTAL: 60 PERIODS

OUTCOMES:

On completion of this lab course, the student would be able to

- Analyze the performance of simple optical link by measurement of losses and Analyzing the mode characteristics of fiber
- Analyze the Eye Pattern, Pulse broadening of optical fiber and the impact on BER
- Estimate the Wireless Channel Characteristics and Analyze the performance of Wireless Communication System
- Understand the intricacies in Microwave System design

ANNA UNIVERSITY: CHENNAI 600 025

BONAFIDE CERTIFICATE

Certified that this project report "AUTONOMOUS ROBOT FOR DELIVERING THE MEDICINE IN HOSPITAL BY USING RASPBERRY PI" is the bonafide work of DHEENA.R, NANDHINI.S, PRAVEEN.S and SRINIJA.J who carried out the project work under my supervision.



SIGNATURE

Mr.MAIDEEN ABDHULKADAR

JEYLANI.M.E.,(Ph.D),



SIGNATURE

Mr.P.RANGASAMY.M.E.,(Ph.D),

HEAD OF THE DEPARTMENT

Associate Professor,

Department of Electronics and

Communication Engineering,

Sree Sakthi Engineering

College, Karamadai,

Coimbatore 641 104.

SUPERVISOR

Associate Professor,

Department of Electronics and

Communication Engineering,

Sree Sakthi Engineering

College, Karamadai,

Coimbatore 641 104.

Submitted for the university examination held on 21.04.22..



INTERNAL EXAMINER



EXTERNAL EXAMINER

ANNA UNIVERSITY:: CHENNAI 600 025

BONAFIDE CERTIFICATE

Certified that this project report "**IOT BASED COAL MINERS SAFETY JACKET USING ESP32**" is the bonafide work of **MAREES KUMAR.J, PAVIPRIYA.G, PREETHIS** and **SRIPATHY.S** who carried out the project work under my supervision.



SIGNATURE

Mr.A.MAIDEEN ABDHULKADER
JEYLANI,M.E.,(Ph.D),

HEAD OF THE DEPARTMENT
Assistant Professor,

Department of Electronics and
Communication Engineering,
Sree Sakthi Engineering College,
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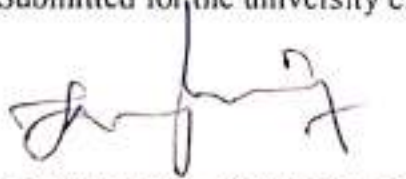
SIGNATURE

Mrs.S.SASIKALA.,M.E.,(Ph.D),

SUPERVISOR
Assistant Professor,

Department of Electronics and
Communication Engineering,
Sree Sakthi Engineering college,
Karamadai,
Coimbatore 641 104.

Submitted for the university examination held on 21/06/22.



INTERNAL EXAMINER



EXTERNAL EXAMINER

Department of ECE & EEE
Cordially invite you for

Two Days Hands on Training on

Embedded Systems and Its Applications

Resource Person


Er. G. SANJAY

SAI Incubation Centre, Coimbatore

30.09.2021, 01.10.2021






PRINCIPAL
Dr. R. PRABHU
PRINCIPAL,
SAKTHI ENGINEERING COL
COIMBATORE-641 104

Coimbatore's No-1 Industry Integrated Campus



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ENGINEERING COLLEGE**

Approved by AKTE, Affiliated to Anna University, Chennai
Accredited by NAAC with B++ Grade

*Department of ECE & EEE
Cordially invite you for*

Two Days Hands on Training on

Raspberry Pi and Its Applications



Er. G. SANJAY

SAI Incubation Centre, Coimbatore

23.09.2021 & 24.09.2021

****Exclusive for Department of EEE & ECE Students***

Dr S.KARTHIKEYAN
Correspondent

Dr R.PRABHU
Principal

Mr N.PRASANAN
CEO

Coimbatore's No-1 Industry Integrated Campus



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Accredited by NAAC with B++ Grade

Department of ECE & EEE

Cordially invite you all for

Webinar on

INDUSTRIAL IoT



Er. G. SANJAY

SAI Incubation Centre, Coimbatore

18.09.2021

11.30 am-12.30 pm

***EXCLUSIVE FOR DEPARTMENT OF EEE & ECE SSEC STUDENTS**

Dr S.KARTHIKEYAN
Correspondent

Dr R.PRABHU
Principal

Mr N.PRASANNAN
CEO



SREE SAKTHI ENGINEERING COLLEGE

KARAMADAI, COIMBATORE – 641 104. NAAC ACCREDITED

Approved by AICTE, New Delhi and Affiliated to Anna university)

SEMINAR ON

“WORLD JOB OPPORTUNITIES FOR ENGINEERING STUDENTS”

Organized by Department of Electronics and Communication Engineering &
Electrical and Electronics Engineering

CHIEF GUEST

R.S.VENKATACHALAM.,

MANAGING DIRECTOR

M/S WIZAARD SYSTEM, COIMBATORE.

Date: **28.08.2021**

Time : **10.30AM to 12.30pm**

“We cordially invite you to make this event a grand Success”

Mr. PRASSANA

CEO

Dr. R. PRABHU

Principal

Dr. S. KARTHIKEYAN

Correspondent

Shri. N. DHARMALINGAM

Chairman

Coimbatore's No-1 Industry Integrated Campus



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Department of ECE & EEE

Cordially Invites You all for

Virtual Seminar on the topic of

"World Job Opportunities for Engineering Students"



Resource Person

MR.R.S.VENKATACHALAM

Managing Director,
Wizaard System, Coimbatore

Date :28.08.2021

Time: 10.45am



Google Meet

★Exclusive for ECE& EEE SSEC Students

Dr.S.KARTHIKEYAN
CORRESPONDENT

Dr.R.PRABHU
PRINCIPAL

Mr.N.PRASANAN
CEO


SREE SAKTHI ENGINEERING COLLEGE

Certificate of Participation

This is to Certify that Mr/Ms/ A.Sowmya has Participated in the Seminar on "WORLD JOB OPPORTUNITIES FOR ENGINEERING STUDENTS" organized by the Department of ECE & EEE at Sree Sakthi Engineering College, Karamadai held on 28.08.2021 .



DR.S.BHARATHIDASAN
HOD/ECE



MRS.P.MALARVIZHI
HOD/EEE

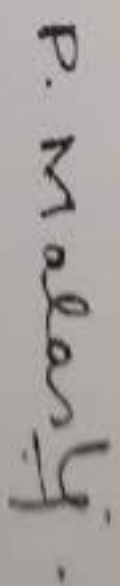
SREE SAKTHI ENGINEERING COLLEGE

Certificate of Participation

This is to Certify that Mr/Ms/ Aneasha M has Participated in the Seminar on "WORLD JOB OPPORTUNITIES FOR ENGINEERING STUDENTS" organized by the Department of ECE & EEE at Sree Sakthi Engineering College, Karamadai held on 28.08.2021.



DR.S.BHARATHIDASAN
HOD/ECE



Mrs.P.MALARVIZHI
HOD/EEE





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Affiliated to Anna University, Chennai), Karamadai, Coimbatore - 641104

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING
INTERNSHIP

SNO	Academic Year	REG NO	NAME	COMPANY	NO OF PARTICIPANTS
1	2021-22	713618106007	PAVIPRIYA G	CASAGRAND PVT LTD,CHENNAI	3
3		713618106009	PREETHI S		
4		713618106011	SRINJA J		

PRINCIPAL
Dr R. PRABHU
PRINCIPAL,
SAKTHI ENGINEERING COL
COIMBATORE-641 104

Date:22nd December, 2021

To:

Ms.Preethi

6/56,M.palada and post,nanjanadu,
Nilgiris, TAMILNADU – 643 004.

Dear Ms.Preethi,

Sub: Consultancy Services – Reg

We take pleasure in appointing you as a **Consultant – Telemarketing** with Casagrand Builder Private Limited on the terms and conditions mutually discussed and agreed upon and you will join us at our **CHENNAI** office.

The consolidated pay offered is **Rs.16,698/- (Sixteen Thousand Six Hundred and Ninety Eight Rupees Only)** per month.

1. The date of starting your services would be **24th December, 2021** and this agreement is for a period of three month ending **23rd March, 2022**. Please let us know your acceptance by signing a copy of this letter. The below mentioned are the terms and conditions of the agreement.
2. You shall be engaged as **Consultant – Telemarketing**.
3. On completion of the above agreement period, your performance will be evaluated and if found satisfactory by your Reporting Manager he / she will recommend you to be considered for full time employment as per company terms and conditions. You will then be taken at the level of **Junior Executive** only and will be on probation as per the service terms of the company.
4. You will work on all working days during the period mentioned, and in no circumstances may the consultant engage himself in any other business whatever whilst so engaged.
5. You shall not indulge in any act or omission, which is likely to harm the reputation of the company.

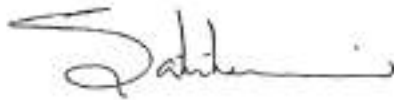
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6. You shall serve the company loyally, faithfully and diligently and shall at all times safeguard and protect the interest of the company.
7. You shall not be entitled for benefits like Gratuity, PF and any other fringe benefits.
8. Either party will be at liberty to terminate the agreement by giving a week's notice in writing with or without assigning any reasons.
9. You will be required to sign a Non-Disclosure Agreement and our Code of Conduct and adhere to its terms and conditions.

We take great pleasure in welcoming you to our Organization.

For Casagrand Builder Private Limited



Satish Xavier
Vice President – Human Resources



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SREE SAKTHI ENGINEERING COLLEGE

TNEA Admission Code **2973**

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COIMBATORE- 641104, INDIA | Web : www.sreesakthi.edu.in

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INDUSTRIAL VISIT REPORT 2022

PLACE: KOCHIN ,VAGAMON(KERALA)

NUMBER OF DAYS : 3 (21.04.2022 to 23.04.2022)

NUMBER OF STUDENTS : 37

NUMBER OF FACULTIES ACCOMPANIED : 3

**DEAPRTMENT : ELECTIRICAL AND ELECTRONICS ENGINEERING
& ELECTRONICS AND COMMUNICATION ENGINEERING**

**INDUSTRY VISITED: KERALA ELECTRICAL AND ALLIED
ENGINEERING LTD (KEL)**

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Journey started from **SREE SAKTHI COLLEGE** towards **Vipin Beach** on 20.04.2022 at 11 pm.

DAY 1 VISIT (21-0402022)

VIPIN BEACH

KERALA ELECTRICAL AND ALLIED ENGINEERING (KEL)

LULU MALL


MARINE DRIVE

Reached **Vipin Beach Kochin** on 21.04.2022 around 6am. enjoyed view of beach and sunrise had fun till 8am.



Checked in to **HOTEL CAPE TOWN KOCHIN** around 8.30am refreshed and had a breakfast on 21.04.2022 around 10 am.




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reached industry **KERALA ELECTRICAL AND ALLIED UNITS (KEL)MAMALA** around 11am.

ABOUT KERALA ELECTRICAL AND ALLIED UNIT MAMALA

The division established in 1969 at Mamala, about 15 km from Kochi. This unit was initiated with technical assistance from Bharat Heavy Electricals to manufacture distribution transformers. Now it is one of the major players in the transformer industry. This unit is one of the first transformer industry in Kerala to obtain BIS certification for distribution transformers and first few in India to get ISO 9001 certification.



There we visited two division **TRANSFORMER DIVISION AND STRUCTURAL DIVISION**. The Structural Engineering Division of KEL Mamala Unit, specializes in the design, fabrication and commissioning of hydraulic gates and hoists and their regulatory utilities used in dams for power and irrigation projects.




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After completing Industrial visit we went to LULU MALL. we played in ROLLER COASTER, TINY CAR GAME and HAD METRO TRAIN TRAVEL of entire LULU MALL. we had shopping spent nearly 4 hours in LULU MALL.



Around 5 pm we left LULU MALL and travelled towards **MARINE DRIVE** at **KOCHIN**. We enjoyed 3 hours in that place. we had DJ at boating around 1 hour.



After boating we reached capetown hotel around 7.30 pm. we had dinner and we stayed in hotel on 21st night.


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DAY 2 VISIT

CHOTANIKARAI TEMPLE

HILL PALACE MUSEUM

Next day after having breakfast we moved towards CHOTANIKARAI TEMPLE. We reached CHOTANIKARAI TEMPLE AROUND 10.30am.

ABOUT CHOTANIKARAI TEMPLE

The Chottanikkara Devi Temple is a famous temple of mother goddess Bhagavati. She is a form of Mahalakshmi. She is believed to be residing in Chottanikkara along with her husband Maha Vishnu. The temple is located at Chottanikkara, a southern suburb of the city of Kochi in the state of Kerala, India and is one of the most popular temples in the state. In terms of temple architecture, Chottanikkara temple stands out to be an ultimate testimonial for the ancient vishwakarma sthaphathis (wooden sculpture) along with Sabarimala temple. Sree Mahamaya Bhagavati (Adi Parashakti), the goddess of saraswati, Lakshmi and Parvati is one of the most popular deities in Kerala and the supreme mother goddess in Hinduism. Chottanikkara Devi (Melekavu Bhagavathi) is worshipped at the temple, in three different forms: as Mahasaraswati in the morning, draped in white; Mahalakshmi at noon, draped in crimson; and as Mahakali in the evening, decked in blue.



Goddess 'Keezhkkaavu bhagavathi' is believed to be Bhadrakali, in her fierce form or Ugra form. Bhadrakali, is a form of mother Kali, supposed to be born from the third eye of lord Shiva, to kill the demon king Darika. People suffering from mental illnesses and commonly visit the temple, as Chottanikkara devi is said to cure her devotees. Guruthi pooja is a ritual done at late evening to invoke

goddess Mahakali. Earlier 'Guruthi Pooja' was done only on Fridays. But nowadays, it is performed every day.

After CHOTANIKARAI temple we travelled towards HILL PALACE MUSEUM in Tripunithura .**Hill Palace** is an archaeological museum and palace located in the Tripunithura neighbourhood of Kochi city . It is the largest archeological museum in the state and was the imperial administrative office and official residence of the Cochin Maharaja. Built in 1865, the palace complex consists of 49 buildings in the traditional architectural style, spreading across 54 acres (220,000 m²). The complex has an archaeological museum, a heritage museum, a deer park, a pre-historic park and a children's park. The campus of the museum is home to several rare species of medicinal plants. Presently the palace has been converted into a museum by The Kerala State Archaeology Department and is open to public. The palace is about 10 kilometres (6.2 mi) from the city centre and is approachable by road and rail.



The Centre for Heritage Studies (CHS), an autonomous research and training institute set up by the Department of Cultural Affairs, Government of Kerala also functions at the site. CHS is designated as the 'Manuscript Conservation Centre' (MCC) and Manuscript Resource Centre (MRC) by the National Mission for Manuscripts.

We spend 1 and half days in KOCHIN climate was very hot. After museum we travelled towards **VAGAMON**.we had lunch around 2pm.We reached VAGAMON around 7 pm on 22.04.2022.we had dinner and reached SRI DEVI HOTEL VAGAMON around 9.00 pm.After refreshing we had campfire from 9.30 pm till 10.30 pm.


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we stayed in SREE DEVI RESORT.

DAY 3 VISIT : 23.04.2022

IDDUKI DAM

TUNNEL WALK ULLUPONI

SUCIDE POINT

PINE FOREST


BOTANICAL GARDEN

We had morning walk around 7am around SRIDEVI RESORT.we checked out from resort around 9am.we had breakfast around 9.30 am.we had JEEP SAFARI and travelled towards EDUKKI DAM and had ULLUPONI tunnel walk.


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We enjoyed the forest atmosphere .Pine trees as very light and they had used to make the boats. They are charging RS.20 per adult and kids under 5yrs for free .The trees are very long and also they are planted in short distance .


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After seeing PINE FOREST we had shopping and had dinner at VAGAMON. again we had CAMP FIRE DJ around 9.30 pm.



After completing camp fire on 3rd day we travelled towards Coimbatore around 10.30 pm .we reached SREE SAKTHI ENGINEERING COLLEGE around 5.40 am on 24.4.2022 (Saturday).


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SREE SAKTHI ENGINEERING COLLEGE, KARAMADAI

DEPARTMENT OF ECE

VALUE ADDED COURSES ON PCB DESIGN

S.No	REG.NO	NAME	18.6.21	19.6.21	20.6.21	21.6.21	22.6.21	23.6.21	24.6.21
1	712618106001	DHEENA R	/	/	/	/	/	/	/
2	712618106003	MAREES KUMAR J	/	/	/	/	/	/	/
3	712618106006	NANDHINI S	/	/	/	/	/	/	/
4	712618106037	PAVTPRIYA G	/	/	/	/	/	/	/
5	712618106008	PRAVEEN S	/	/	/	/	/	/	/
6	712618106009	PREETHI S	/	/	/	/	/	/	/
7	712618106011	SRINIDA J	/	/	/	/	/	/	/
8	712618106012	SRIPATHY S	/	/	/	/	/	/	/
9	712618106013	ANANTHI	/	/	/	/	/	/	/
10	712619106004	GOKULAKRISHNAN C	/	/	/	/	/	/	/
11	712619106005	GOPINATH R	/	/	/	/	/	/	/
12	712619106008	KANPRIYA P	/	/	/	/	/	/	/
13	712619106009	KARTHEESKSHANKAR S	/	/	/	/	/	/	/
14	712619106010	KEERTHANA K	/	/	/	/	/	/	/
15	712619106011	KIRUTHIKA B	/	/	/	/	/	/	/
16	712619106012	LIBI NANDHINI N	/	/	/	/	/	/	/
17	712619106013	MOHAN K	/	/	/	/	/	/	/
18	712619106015	PANTHALARAJAN M	/	/	/	/	/	/	/
19	712619106016	PRIYADHARSHINI R	/	/	/	/	/	/	/
20	712619106017	RAMKUMAR M	/	/	/	/	/	/	/
21	712619106018	ROSHANAPRIYA S	/	/	/	/	/	/	/
22	712619106019	SANJULA M	/	/	/	/	/	/	/
23	712619106020	SOWMYA A	/	/	/	/	/	/	/
24	712619106022	YASWANTHI	/	/	/	/	/	/	/

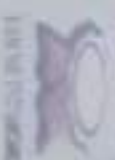

PRINCIPAL
Dr R. PRABHU
PRINCIPAL,
SAKTHI ENGINEERING COL
COIMBATORE-641 104



Ally Tech
A friendly nation

CERTIFICATE of INTERNSHIP

We present this certificate to



P. KANUPRIYA

in appreciation for your successful work as an intern at

.....
Imaga Processing

The internship was conducted between 15-07-2022 and 1-08-22



A. Rajeshwari
Managing Director



Ally Tech
A friendly nation

CERTIFICATE of INTERNSHIP

We present this certificate to

.....MOHAN..K.....

in appreciation for your successful work as an intern at

.....Image Processing.....

The internship was conducted between .15-07-2022 and .01-08-2022



EMP NO: 137/AM/2022

A. Perumal
Managing Director





SREE SAKTHI ENGINEERING COLLEGE

TNEA Admission Code **2673**

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COIMBATORE- 641104, INDIA | Web : www.sreesakthi.edu.in

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DEPARTMENT OF MECHANICAL ENGINEERING

ACADEMIC YEAR-2021-2022 (ODD SEMESTER)

2.3.1 Experimental learning:

The process of learning through doing is known as experiential learning. Students are better able to relate concepts and information taught in the classroom to actual circumstances when they are involved in practical activities and reflection.

Opportunities for experiential learning come in both course- and non-course-based formats and might include concluding activities like internships, student teaching, capstone projects, undergraduate research, and service learning, to mention a few.

1. Improved comprehension of the course material
2. A more expansive perspective of the world and a sense of community Understanding of one's own abilities, interests, passions, and values
3. Possibilities to work with various organizations and persons
4. Positive business habits and competencies
5. The satisfaction of satisfying needs in the community
6. Positivity and leadership abilities

Project presentation

Title: Wiper machine by four bar mechanism

Sridhar,Suryakanth (iii year Mech)



Title: Multi axis cutting mechanism
Saravanakumar,Saravanan (iii year Mech)




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HINDUSTHAN INSTITUTE OF TECHNOLOGY
Coimbatore - 641032

(An Autonomous Institution)
 Approved by AICTE, New Delhi, Affiliated to Anna University, Chennai.
 (Accredited with 'A' Grade by NAAC, Accredited by NBA)

National level Technical Symposium
 Organized by Department of Mechanical Engineering

MECHTRIGERZ-2K22

Certificate of Participation

This is to certify that Mr/Ms P. VELRAT
 of SREE SAKTHI ENGINEERING COLLEGE has Actively Participated in the
 Events of **MECHTRIGERZ-2K22** held on 25th March 2022

[Signature]
 Dr. P. V. Ganesha Kumar
 Coordinator



[Signature]
 Prof. R. V. Rangarajan
 Organizing Secretary

[Signature]
 Dr. S. R. Sasavana
 Convener & Head

[Signature]
PRINCIPAL
Dr R. PRABHU
 PRINCIPAL,
 SAKTHI ENGINEERING COL
 COIMBATORE-641 104



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National level Technical Symposium
 Organized by Department of Mechanical Engineering

MECHTRIGERZ-2K22

Certificate of Merit

This is to certify that Mr/Ms A AHAMED RINEESH
 of SREE SAKTHI ENGINEERING COLLEGE has been Awarded/ Presented a
 paper in the Event / Entitled TECHNICAL QUIZ and secured
FIRST place in MECHTRIGERZ-2K22 held on 25th March 2022

[Signature]

Prof. K. V. Ganeshkumar
 Co-ordinator



[Signature]
 Anil V. Manjunathan
 Organizing Secretary

[Signature]

Dr. S. Srinivasan
 Convenor & Head

[Signature]
PRINCIPAL

Dr R. PRABHU
 PRINCIPAL,
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 COIMBATORE-641 104

ONE-DAY NATIONAL WORKSHOP ON
**INSIGHTS
FEA**
(FINITE ELEMENT ANALYSIS)

**CERTIFICATE
OF PARTICIPATION**

This is to certify Br. /Mr. / Ms.

SARAVANAN. M - III MECH. from SREE

SAKTHI ENGINEERING COLLEGE has participated in One-Day

National Workshop on "INSIGHTS OF FEA" organized by Department of Mechanical

Engineering, Rathinam Technical Campus on 5th May 2022.

A. Nagamani
A. NAGAMANI
CO-ORDINATOR

Prof. A. Thirumoorthy
PROF. A. THIRUMOORTHY
CONVENOR

Dr. B. Nagaraj
DR. B. NAGARAJ
PRINCIPAL

RATHINAM TECHNICAL CAMPUS
AFFILIATED TO ANNA UNIVERSITY, APPROVED BY AICTE, NEW DELHI
RATHINAM TECHZONE, POLLACHI ROAD, SACHANARI (POST), COIMBATORE - 641 02, TAMIL NADU
www.rathinamcollege.edu.in

Dr. R. Prabhu
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SREE SAKTHI ENGINEERING COLLEGE, KARAMADAI

(Accredited by NAAC with B++ Grade, Approved by AICTE, Affiliated to Anna University, Chennai)

DEPARTMENT OF ELECTRICAL & ELECTRONICS ENGINEERING

ACADEMIC YEAR: 2021-2022 (EVEN)

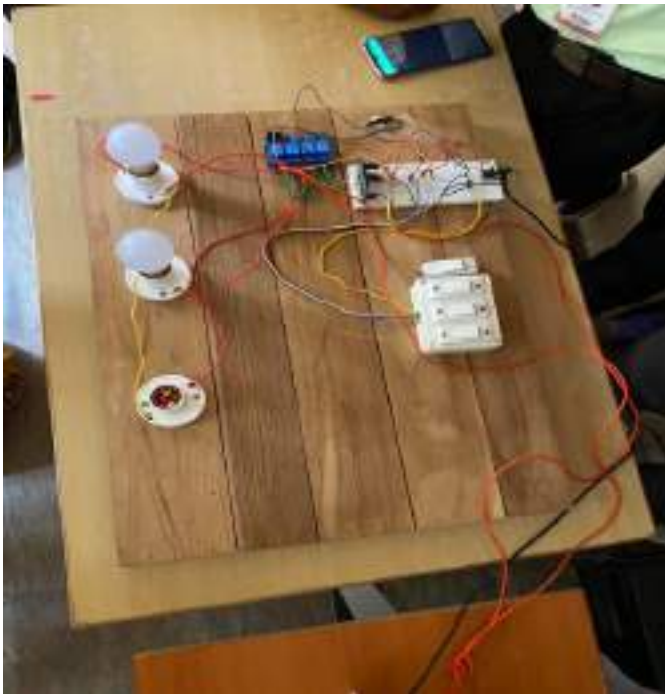
YEAR / SEM: III/ VI

FACULTY GUIDE: Mr. S.PRAVEEN KUMMAR

DATE: 09.04.2022


DAY: Saturday

MODEL BASED LEARNING Topic: IoT based Home Automation



Batch Students:

- K.ARUNA DEVI
- M.DHANASEKAR
- K.GOVINDHARAJ
- R.KANNATHAS
- E.AJITH


PRINCIPAL
Dr.R. PRABHU
PRINCIPAL,
SAKTHI ENGINEERING COL
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ELECTROPHOENIX'22



DEPARTMENT OF EEE


DEPARTMENT OF ELECTRICAL SCIENCES (ECE & EEE)

Certificate

This is to certify that Mr/Ms K. GOVINDHARAJ
 Department of EEE-III from SREE SAKTHI ENGINEERING COLLEGE has
 Participated/ Won III prize in the event of PROJECT EXPO at
 National Level Technical Symposium "ELECTROPHOENIX'22" organized by Department of
 Electrical Sciences (ECE & EEE), Adithya Institute of Technology, on 18th May 2022.


 Co-ordinator


 HoD


 PRINCIPAL
Dr R. PRABHU
 PRINCIPAL,
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ELECTROPHOENIX'22



DEPARTMENT OF ELECTRICAL SCIENCES (ECE & EEE)

Certificate

This is to certify that Mr/Ms M. SURYA
Department of EEE - III from SREE SAKTHI ENGINEERING COLLEGE has
Participated/ Won - prize in the event of PROJECT EXPO at
National Level Technical Symposium "ELECTROPHOENIX'22" organized by Department of
Electrical Sciences (ECE & EEE), Adithya Institute of Technology, on 18th May 2022.

T. J. J.
Co-ordinator

A. K. J.
HoD

R. Prabhu
PRINCIPAL

Dr R. PRABHU
PRINCIPAL,
SAKTHI ENGINEERING COL
COIMBATORE-641 104

D. Somasundar
Principal



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ELECTROPHOENIX'22



DEPARTMENT OF ELECTRICAL SCIENCES (ECE & EEE)

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This is to certify that Mr/Ms E. AJITH
Department of EEE - III from SREE SAKTHI ENGINEERING COLLEGE has
Participated/ Won Ist prize in the event of PROJECT Expo at
National Level Technical Symposium "ELECTROPHOENIX'22" organized by Department of
Electrical Sciences (ECE & EEE), Adithya Institute of Technology, on 18th May 2022.


Co-ordinator


HoD


PRINCIPAL

Dr. R. PRABHU

PRINCIPAL,

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ELECTROPHOENIX'22

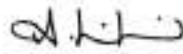


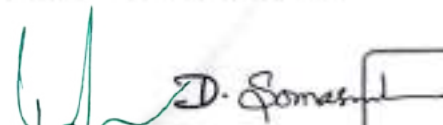
DEPARTMENT OF ELECTRICAL SCIENCES (ECE & EEE)

Certificate

This is to certify that Mr/Ms M. DHANA SEKARAN
Department of EEE - III from SREE SAKTHI ENGINEERING COLLEGE has
Participated/ Won - prize in the event of PROJECT Expo at
National Level Technical Symposium "ELECTROPHOENIX'22" organized by Department of
Electrical Sciences (ECE & EEE), Adithya Institute of Technology, on 18th May 2022.


Co-ordinator


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PRINCIPAL Principal
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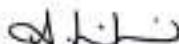


DEPARTMENT OF ELECTRICAL SCIENCES (ECE & EEE)

Certificate

This is to certify that Mr/Ms K. ARUNA DEVI
Department of EEE - III from SREE SAKTHI ENGINEERING COLLEGE has
Participated/ Won - prize in the event of PROJECT EXPO
National Level Technical Symposium "ELECTROPHOENIX'22" organized by Department of
Electrical Sciences (ECE & EEE), Adithya Institute of Technology, on 18th May 2022.


Co-ordinator


HoD


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PRINCIPAL,
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Principal